ABSTRACT

Configurable Function Implementing System and Digital to Analogue Converters

Apparatus for converting an M-bit digital signal into an analogue signal. The apparatus comprising means 12,13 for mapping the M-bit digital signal to first and second digital values, so that the ratio of the first to the second digital value is equal to or approximates the value of the M-bit digital signal. First and second digital to analogue converters 14,15 are provided, the first digital to analogue converter 14 having an input for receiving said first digital value and the second digital to analogue converter 15 having an input for receiving said second digital value. Circuit means 16 is coupled to the analogue outputs of the digital to analogue converters 14,15 for dividing one of the analogue outputs by the other, and for providing the result to an output.

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Figure 5